



ST. JOSEPH'S INDIAN COMPOSITE PU COLLEGE

#23, Vittal Mallya Road, Bangalore – 560001.

SUBJECT: ELECTRONICS (40)

II PUC - CLASS

PORTION FOR FIRST QUARTERLY TEST – 2026

SUBJECT: ELECTRONICS

Ch 1 - Field Effect Transistor (FET)

Ch 2 – Bipolar Junction

Transistor (BJT) biasing

Ch 3 – Transistor Amplifiers

Ch 10 – Digital Electronics